

JUN 0 8 2005

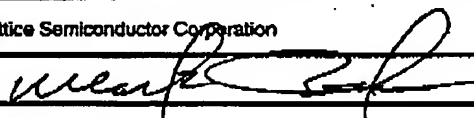
PTO/SB/21 (09-04)

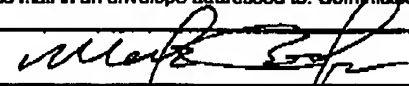
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TRANSMITTAL FORM	Application Number	10/006,559	
	Filing Date	12/03/2001	
	First Named Inventor	Antony Davies	
	Art Unit	2637	
	Examiner Name	Qutbuddin Ghulamali	
(to be used for all correspondence after initial filing)			
Total Number of Pages in This Submission	19	Attorney Docket Number	01-52

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Firm Name	Lattice Semiconductor Corporation	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Antony Davies

Application No.: 10/006,559

Filed: 12/03/2001

For PHASE SELECTION UNIT WITHIN PHASE LOCK
LOOP WITH MINIMIZED NUMBER OF PHASE
INTERPOLATORS

Examiner: Qutbuddin Ghulamali

Art Unit: 2637

Attorney Docket No. 01-52

RESPONSE

Applicants respond to the Office action mailed 3/09/05 as follows.